

19/20  
NOVEMBER, 2025  
FRANKFURT DORINT PALLAS HOTEL  
WIESBADEN

**DVN**  
Sensing & Applications

# DVN AUTOMOTIVE SENSING & APPLICATIONS

CONFERENCE & EXPO

8<sup>TH</sup> DVN  
WORKSHOP

EMERGING MARKETS:  
L2+ / L3 / ROBOTAXIS?



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— Glass with vision

# The Intelligent Eye for the Future



**Global 3D CIS Leadership:  
Forged through Automotive and  
Consumer Electronics Collaboration.**

Single Photon Avalanche Diodes (SPADs) are core components of 3D perception in modern electronic devices, enabling smarter cars, phones, robots, autonomous control, and human-machine interaction. Established in 2018 to commercialize leading edge SPAD research, Adaps Photonics creates eyes for a smart future by providing SPAD-based dToF sensor chips and system solutions. Our products deliver industry-leading accuracy, distance, and power efficiency.

Adaps Photonics provides a family of SPAD-based sensor products: SiPM, 3D-stacked dToF module, and all-in-one dToF depth sensor. Our most advanced 3D-stacked module consists of a BSI sensor array die hybrid-bonded to a custom DSP die to achieve the highest performance and density in the industry.

Top-tier companies in the automotive and consumer electronics industries have chosen Adaps Photonics for our competence to deliver superior products in volume by sourcing from multiple foundries and a robust global supply chain. Our development process complies with ISO 9001. Our automotive products, produced by a supply chain qualified for IATF16949, are set to be certified as qualified for AEC-Q100 and AEC-Q102.

## Technical and Mass Production Competence

Our vision is to empower smartphones, XR devices, automobiles, and all kinds of robotics with the most advanced, high performance, cost-efficient, and highly reliable dToF depth sensors

- Focus on innovative IC design and productization
- World leading proprietary advanced SPAD Technology
- Many dToF products in mass production: SiPM, SPADIS, and all-in-one dToF sensor
- Full stack capability from device, IC design, System design and Algorithm

SiPM	SPADIS	Few-point dToF Chip & Module

# Infrared for Real World Safety



## Compal CDAT™ AI Algorithms

(Classification, Distance, Action, Tracking)



Lowest cost FMVSS127 Solution  
AutoHD resolution to meet L3/L4

- Nighttime AEB
- Forward Collision Warning
- Occluded Pedestrians
- Animal Classification
- Entering/Exiting Tunnels
- Headlight Glare
- Construction Sites
- Blinding Sunlight Glare
- Fog, Dust & Smoke
- Poor Weather
- Dirty Windshield
- Debris on the road
- Poor visibility

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# FortSense

## FortSense Co., Ltd

A world leading LiDAR sensing chip company.

Pioneering automotive-grade SPAD-SoC innovation.

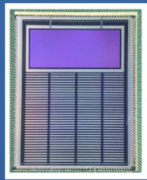
Founded in 2017, FortSense is a high-tech semiconductor company specializing in chip-level R&D of core technologies for all-solid-state LiDAR.

The company has successfully developed and mass-produced AEC-Q100 qualified SPAD-SoC chips, achieving one of the world's leading large-scale deployments in passenger vehicles.

With over 70% of its team dedicated to R&D, FortSense brings together top experts across SPAD device processes, mixed-signal chip design, system software and hardware, optics, and LiDAR system simulation. The core chip team boasts over 20 years of industry experience, having developed multiple commercially successful, high-volume semiconductor products.

FortSense delivers a portfolio of innovative all-solid-state LiDAR SPAD-SoC chips and the proprietary OmniLumi™ all-solid-state light beam steering technology, tailored for diverse application scenarios.

By leveraging FortSense's chips, partners can rapidly develop complete LiDAR systems or compact, cost-efficient all-solid-state LiDAR modules for robots, drones, and autonomous intelligent devices — empowering machines to perceive their surroundings, act intelligently, and unlock new market potential.



All-Solid-State FLASH LiDAR (FL6031, FL6032Q, FL6532, FL6552)

All-Solid-State Long-Range LiDAR (FL8132Q)

Semi-Solid-State Long-Range LiDAR (FL7132Q, FL7532)



FortSense brings over 20 years of industry experience in mixed-signal IC design and high-volume market production



The forefront of laser radar core chip and system technology development



FortSense team covers the full technology chain from silicon to system, with experts in wafer processing, chip design, optical, LiDAR systems, and algorithm



With over 500 proprietary intellectual property rights, including more than 300 authorized patent

### Typical Customer End-Products



VanJee WLR-750 All-Solid-State LiDAR



ECARX All-Solid-State LiDAR



ZhiFenLing AS5000 All-Solid-State LiDAR

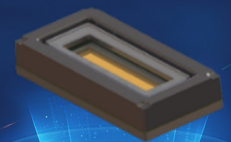
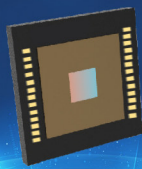
## SPAD-SoC Product Series

Industry-first, mass production, leading the market.

Line SPAD-SoC Sensor Series

Area Array SPAD-SoC Sensor Series

Line Array SPAD-SoC Sensor Series



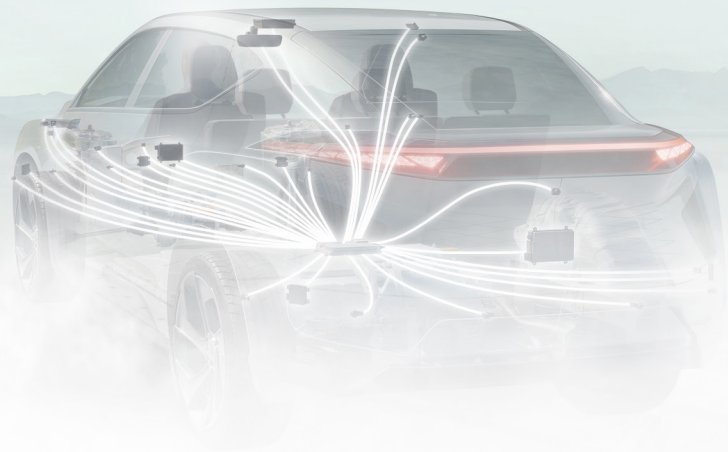
Email : [Public@fortsense.com](mailto:Public@fortsense.com)

Website: <https://www.fortsense.hk>

# Creating a better world of mobility, responsibly.

We see a future where everyone can live and move without limitations. We are developing technologies, systems and concepts that make vehicles safer and cleaner, while serving our communities, the planet and, above all, people.

**Forward. For all.**

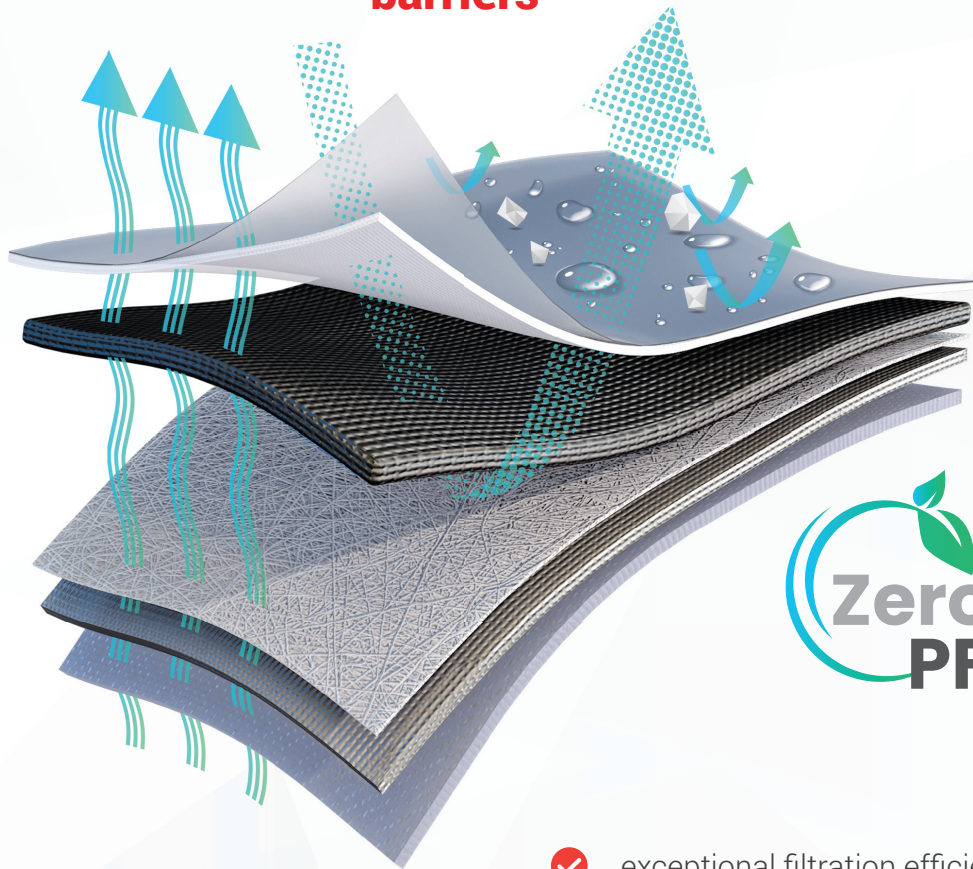


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# BOROFLOAT® & LiDAR systems

## A Union of Inspiration & Quality

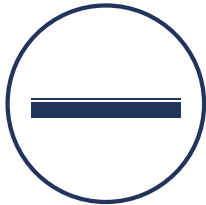
BOROFLOAT® 33 from Germany – the world’s first floated borosilicate flat glass. It combines superior quality and excellent flatness with outstanding thermal, optical, chemical and mechanical features and these features make BOROFLOAT® 33 a perfect specialty glass solution for LiDAR systems.



Excellent flatness



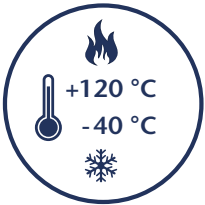
Exceptionally high transmission



Excellent coating performance



Superior mechanical strength



Outstanding thermal resistance





super computing systems

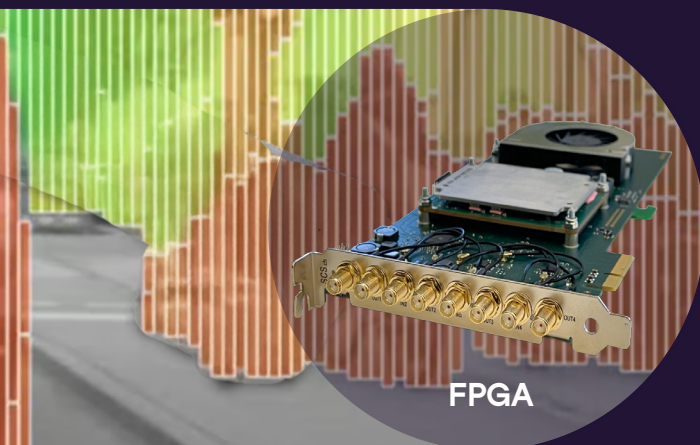
# Expertise in autonomous driving

Supercomputing Systems AG has been supporting OEM and tier 1 automotive manufacturers with driver assistance systems and autonomous driving projects for the last two decades.



## Engineering

- Feasibility studies
- Hardware (Specification, Design, Schematics, Layout, Production)
- Firmware/IP (FPGA, DSP)
- Software (Drivers, Host SW – Windows/Linux)
- Optimizations (FPGA, AIE, ARM, DSP, EVE, Neon)



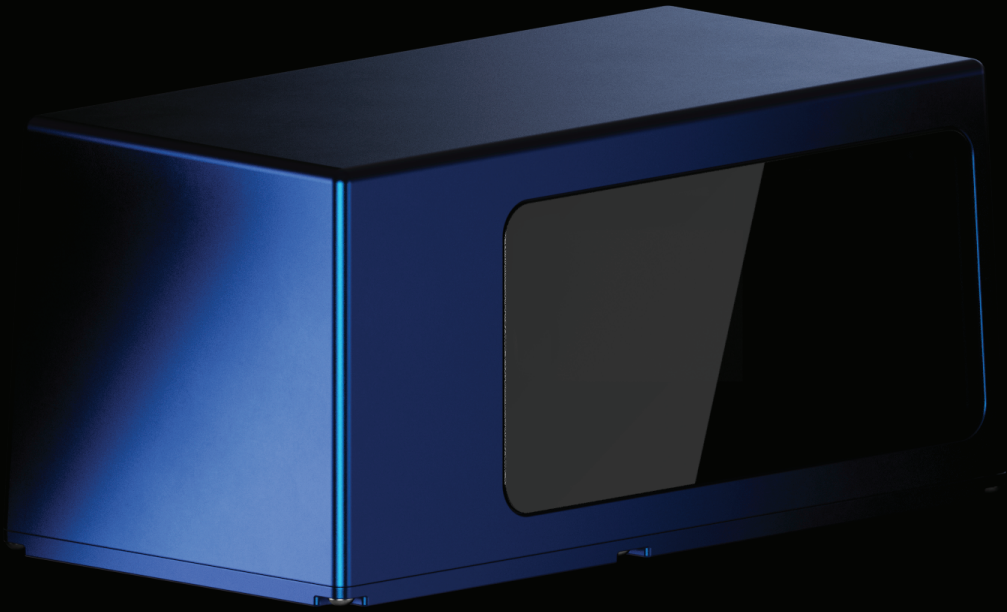
## Measurement Technology

- Capture and replay of data streams (FPGA, e.g. AMD Zynq™ UltraScale+™ MPSoCs)
- 10  $\mu$ s accuracy
- ROS, ADTF Or standalone applications
- FPD Link III and IV, Maxim (GSML 2/3) or Sony Ser/DeS
- CAN, CAN-FD, LIN, BroadRReach, Flexray and GigE

[www.scs.ch](http://www.scs.ch)

Supercomputing Systems AG  
Zürich, Switzerland





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Sensing & Applications

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